

1 SYSTEM AND METHOD FOR TESTING
2 PARAMETERIZED LOGIC CORES

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8 ABSTRACT

9 A system and method for testing a parameterizable logic
10 core are provided in various embodiments. A test controller
11 is configured and arranged to generate a set of random
12 parameter values for the logic core. A netlist is created
13 from the parameterized logic core, and circuit behavior is
14 simulated using the netlist. In other embodiments, selected
15 parameter values are optionally weighted to increase the
16 probability of generating those values, and the parameter
17 set is cloned and mutated when simulation fails.